



**Koneru Lakshmaiah Education Foundation**

(Deemed to be University u/s. 3 of the UGC Act, 1956)

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Ref: KLEF/AR/Ph.D/Engineering/2022-23/590

Date: 29-03-2023

## NOTIFICATION

It is hereby notified that the Vice Chancellor, on the recommendations of the examiners appointed to adjudicate the Ph.D. thesis entitled "**LOW-POWER HIGH-SPEED RESILIENT ARCHITECTURES FOR HIGH SENSITIVE ARITHMETIC CIRCUITS USING MACHINE LEARNING ALGORITHMS FOR ACCURACY ANALYSIS**" submitted by **LAVANYA MADDISSETTI**, Research Scholar, Koneru Lakshmaiah Education Foundation (Deemed to be University) with **Registered Number 15304002**, Department of Electronics and Communication Engineering, under the supervision of **Dr. S. Koteswara Rao**, Professor, Department of ECE, KLEF and **Dr.J.V.R. Ravindra**, Co-Supervisor, for the award of Ph.D. Degree, and on consideration of the report submitted by the Examiners of the VIVA-VOCE Board, has ordered that **LAVANYA MADDISSETTI** be declared qualified for the award of the **Degree of Doctor of Philosophy (Ph.D)** in "**Electronics and Communication Engineering**" in the "**Faculty of Engineering**" of this University.



To  
**LAVANYA MADDISSETTI**,  
Research Scholar,  
Registered Number **15304002**,  
Department of Electronics and Communication Engineering,  
Koneru Lakshmaiah Education Foundation (Deemed to be University).

(Dr.K. Subba Rao)  
Registrar (I/C)

REGISTRAR (I/C)  
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**EXALTING EDUCATION, EPITOMISING EXCELLENCE**

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